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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/761,625

01/22/2004

Meng-An Pan

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3541

32294 7590 06/29/2007
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EXAMINER

NGUYEN, TUAN HOANG

ART UNIT

PAPER NUMBER

2618

MAIL DATE

DELIVERY MODE

06/29/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

<p align="center">Advisory Action Before the Filing of an Appeal Brief</p>	Application No. 10/761,625	Applicant(s) PAN ET AL.	
	Examiner Tuan H. Nguyen	Art Unit 2618	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 02 May 2007 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☒ The period for reply expires 2 months from the mailing date of the final rejection.
 b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
- (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
 (b) ☐ They raise the issue of new matter (see NOTE below);
 (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 (d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
 5. ☐ Applicant's reply has overcome the following rejection(s): _____.
 6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
 7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
 The status of the claim(s) is (or will be) as follows:
 Claim(s) allowed: _____.
 Claim(s) objected to: _____.
 Claim(s) rejected: 1-17.
 Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
 9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
 10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
 12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____.
 13. ☐ Other: _____.

Continuation of 11. does NOT place the application in condition for allowance because: Applicant's arguments have been fully considered but they are not persuasive.

In response to Applicant's remark on pages 2-9, Applicant argues that Pehlke et al. (U.S. PUB. 2002/0136325 hereinafter "Pehlke") reference cited by the Examiner fails to teach or suggest all of the elements of the claims. Specifically, Applicants argue that Pehlke fail to teach or suggest "receiving an instruction to adjust the output power of power amplifier," "powering on or off at least one branch of the power amplifier according to the received instruction," or "amplifying a signal according to the adjusted output power," as recited in claim 1, and similarly recited in claims 8 and 9. Similarly, Pehlke do not teach or suggest "wherein the transistors are arranged in a logarithmic scale, thereby enabling a logarithmic change in output power with the powering on or off of a transistor". Examiner respectfully disagrees with the Applicant argument.

Consider claims 1, 8, and 9, Applicant should refer to Pehlke reference (see figs. 1, 4 and 9 page 5 [0051]) where as the Examiner interpreted "powering on or off at least one branch of the power amplifier or "amplifying a signal according to the adjusted output power," (by selecting the appropriate branch (14), or combinations of branches (14), the effective size of the branched power amplifier (12) may be adjusted as output signal power requirements change. Thus, the branched power amplifier (12) is configurable (read on "instruction") in the sense that different branches, or combinations of branches, may be enabled (read on "power ON") to effect different peak power efficiencies of the power amplifier) according to the received instruction (configuration) to enable a logarithmic change in output power of the amplifier'. Furthermore, the Applicant argues that Pehlke reference cited by the Examiner fails to teach or suggest "the transistors are arranged in a logarithmic scale, thereby enabling a logarithmic change in output power with the powering on or off of a transistor" as recited in claim 15. Examiner respectfully disagrees with the Applicant argument.

Consider claim 15, Applicant should refer to Pehlke reference (fig. 6B page 4 [0045]) where as the Examiner interpreted "the transistors are arranged in a logarithmic scale, thereby enabling a logarithmic change in output power with the powering on or off of a transistor", the output transistor (76B) is coupled to the control transistor (76A) in a manner that causes it to provide the supply current IPA in proportion to the control current IAMIN. The geometry of the output transistor (76B) may be scaled relative to the control transistor (76A) to establish a desired current mirror gain, thus setting the desired relationship between the instantaneous magnitude of the control current IAMIN and the supply current IPA. Therefore, the teaching of the prior art references still read on.

Base on the above rational, it is believed that the claimed limitations are met by the references submitted and therefore, the rejection are maintained..

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